

UPC-PLUS

Thermal Image Analysis Report

Summary	<input type="checkbox"/> Pass			
	<input type="checkbox"/> Fail			
	<input checked="" type="checkbox"/> Pass with Deviation			
	Comment: <u>There is one temperature point marginal passed, the system works properly.</u>			
Test Result Summary				
	Critical	Major	Minor	Enhancement
Defect Found	0	0	0	1
Defect Unsolved	0	0	0	1

Issue date

2019 / 01 / 23

QE Manager

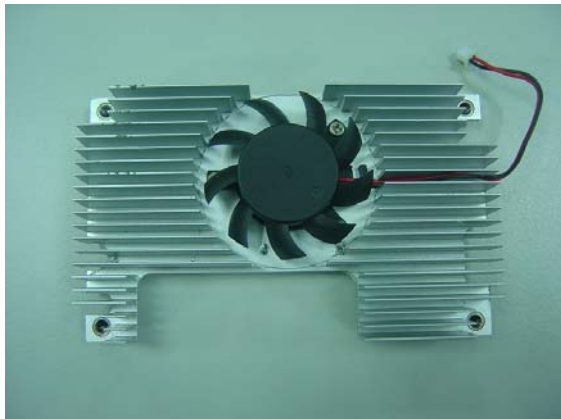
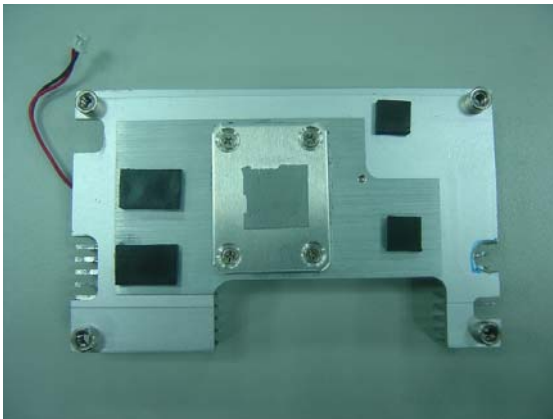
KJ Wang

Test Engineer

Ben Sun

Sample Configuration & Quantity Under Test

- **Model name : UPC-PLUS A0.4**
- **CPU : Intel® APL E3940 2M Cache, up to 1.80 GHz**
- **Memory : LPDDR4 Micron MT53B256M32D1NP-062WT:C**
- **Storage: eMMC 64GB Sandisk SDINBDA4-64G-V**
- **BIOS : UPCPSM0A3**
- **Test Software : Ubuntu 16.04 / Run PassMark Burn In Test 3.2**
- **Power : PS1065-120IB500 12V/5A**
- **Heat Sink & Fan:**



Thermal Image Analysis

1. Test Date: 2019-01-21

2. Test Product: UPC-PLUS A0.4

3. Test Site: AAEON QE Dept.

4. Temperature Measurement:

4.1. 40 Channel Thermal Recorder:

4.1.1 YOKOGAWA Inc,

4.2.2 Model: DA100-13-1D

Date of Calibration: 2018/09/07

Serial Number: 12A323190

4.2. IR Scanner: Infrared Camera

4.2.1 NEC Avio Infrared Technologies Co., Ltd.

4.2.2 Model: Thermo GEAR G100W2-D

Date of Calibration: 2018/11/23

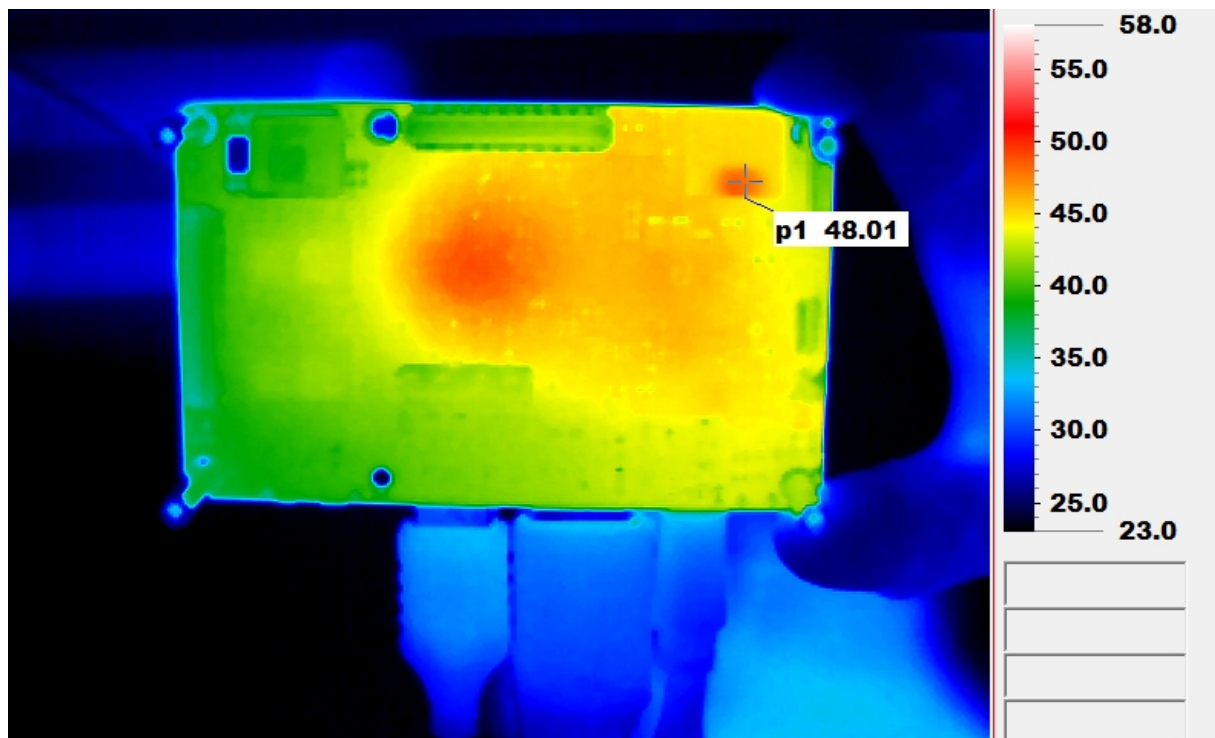
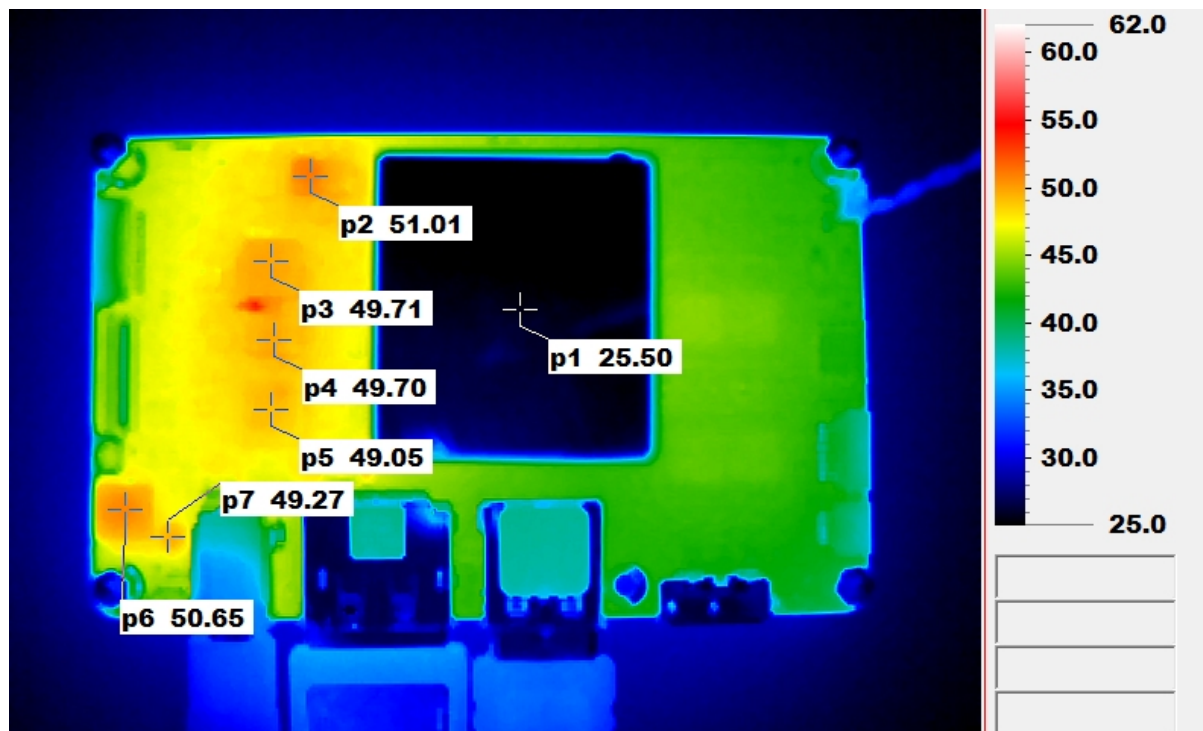
Serial Number: 1051444

5. Test Condition:

Test by DA-100: 25.4°C with Heat Sink & Fan

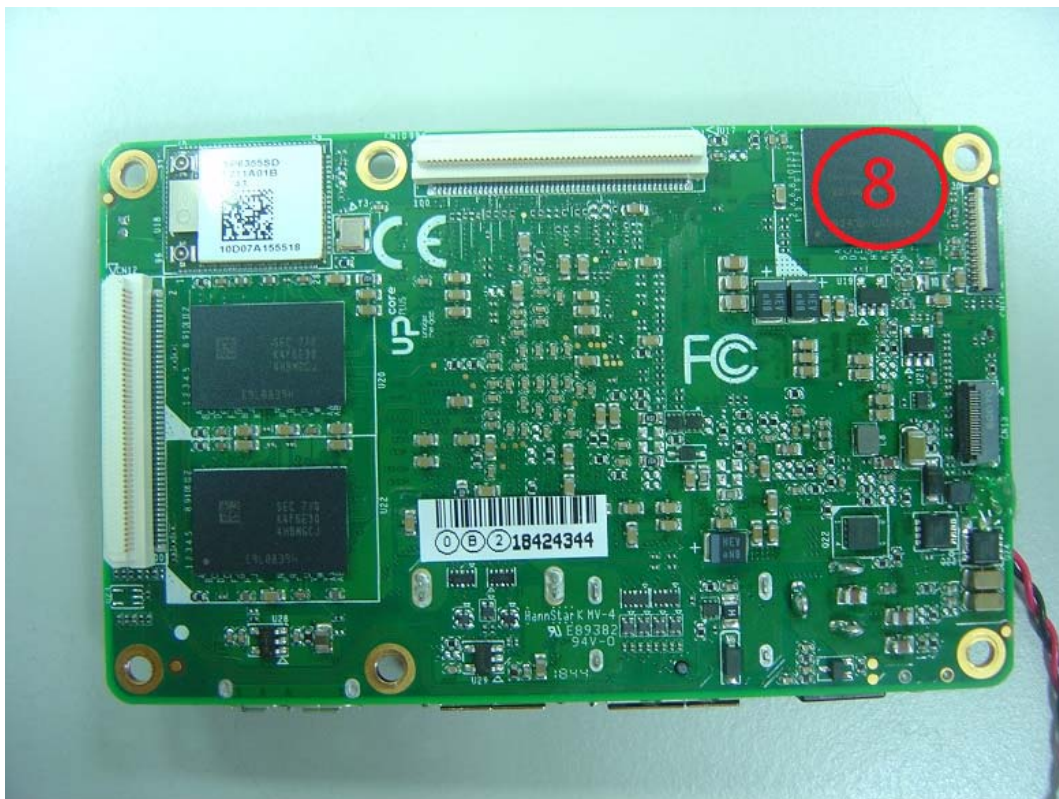
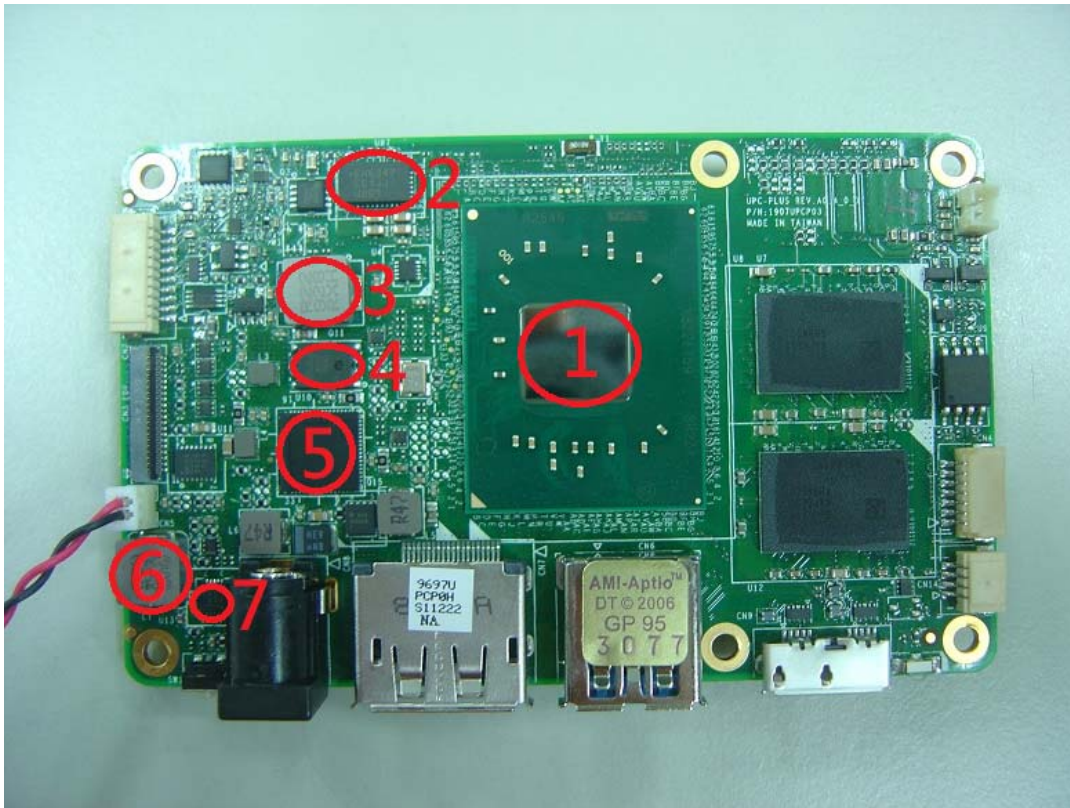
6. Take Picture Time:

After power on 2 hours

Temperature Profile Test:**Component Side:**

Terminal Recorder:

Measuring Thermal Couple Position :



Using YOKOGAWA / DARWIN DA100-100-13-1D test

Point	Position	Describe	T _c (*1) (°C)	T _{AT} (*2)	T _{PT} (*3)	Note
				25.0°C	60°C	
1	U8	(TF)INTEL CPU.Apollo Lake.Celeron N3350.2.4GHz FCBGA1296 SMD.FH8066802980002.SR2Z7	105	35.1	70.1	
2	U87	(TF)IC.Enpirion Power.PWM Buck with .Integrated Induct.QFN 38.SMD.Altera.EN6347QI	100	37.7	72.7	
3	L2	(TF)COIL.0.22uH.DCR=2.8mohm.Idc=23Amp.20%.SMD.6. 95x6.6x2.8mm.CYNTEC.PCMB063T-R22MS	100	29.4	64.4	
4	Q11	(TF)PWR.DUALSMD.N-MOSFET.Vgs1/2=(+/-)20/12V.Vds1/ 2=30V.Id1=13A.Id2=25A.Rds(on)=10.8/3.8mohm.PQFN8.F AIRCHILD.FDMS3664S	125	41.5	76.5	
5	U10	(TF)IC.PMIC.Intel Apollo Lake.DDR=1.1V.VQFN 64P SMD TI.TPS650940A0RSKR.(after&including DC1731)	85	35.2	70.2	
6	L7	(TF)COIL.3.3uH.Idc=6.5A.DCR=18mohm.20%.6.8x7.3x3.0 mm.2P.SMD.CYNTEC.PCMB063T-3R3MS 1211133075	125	34.8	69.8	
7	U13	(TF)IC.High Current Synchronous.Step-down Converter QFN16.3x3mm.SMD.MPS.NB671GQ-Z	100	41.2	76.2	
8	U17	(TF)IC.eMMC 5.1 Flash.64GB.3.3V.FBGA.153P 11.5x13x1mm SMD.Sandisk.SDINBDA4-64G-V	85	42.1	77.1	NOTE4

Note(*):

1. "T_c" indicates the component's case maximum temperature value specified in its datasheet.
2. "T_{AT}" indicates the actual measured temperature under product specification.
3. "T_{PT}" indicates the predicted temperature under 25°C working environmental.
4. Judgment Criteria:
 - **Fail** : T_m > T_c+5°C; The measured value is over specification plus margin.
 - **Margin** : T_c+5°C > T_m > T_c-10°C; The measured value is within specification with margin.
For FANLESS system application, it is strongly recommended to add thermal dissipation design for better reliability.
 - **Pass** : T_m < T_c-10°C; The measured value is with safety margin.
5. RTC battery avoid to put on heat position. Please do not exceed battery temperature specification.
4. Defect NO.